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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10043276	01/14/2002	438	14	2812	

**APPLICANTS: Nakatani Goro; Sakamoto Tatsuya;

**CONTINUING DATA VERIFIED: JML
none

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** FOREIGN APPLICATIONS VERIFIED: JML
JAPAN 2001-6581 01/15/2001

<input type="checkbox"/> DO NOT PUBLISH		<input type="checkbox"/> RESCIND
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 040894-5755
35 USC 115 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiners's initials	JML	
TITLE : Semiconductor device and method for manufacturing the same		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drawn	Figs. Drawn	Print Figs.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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